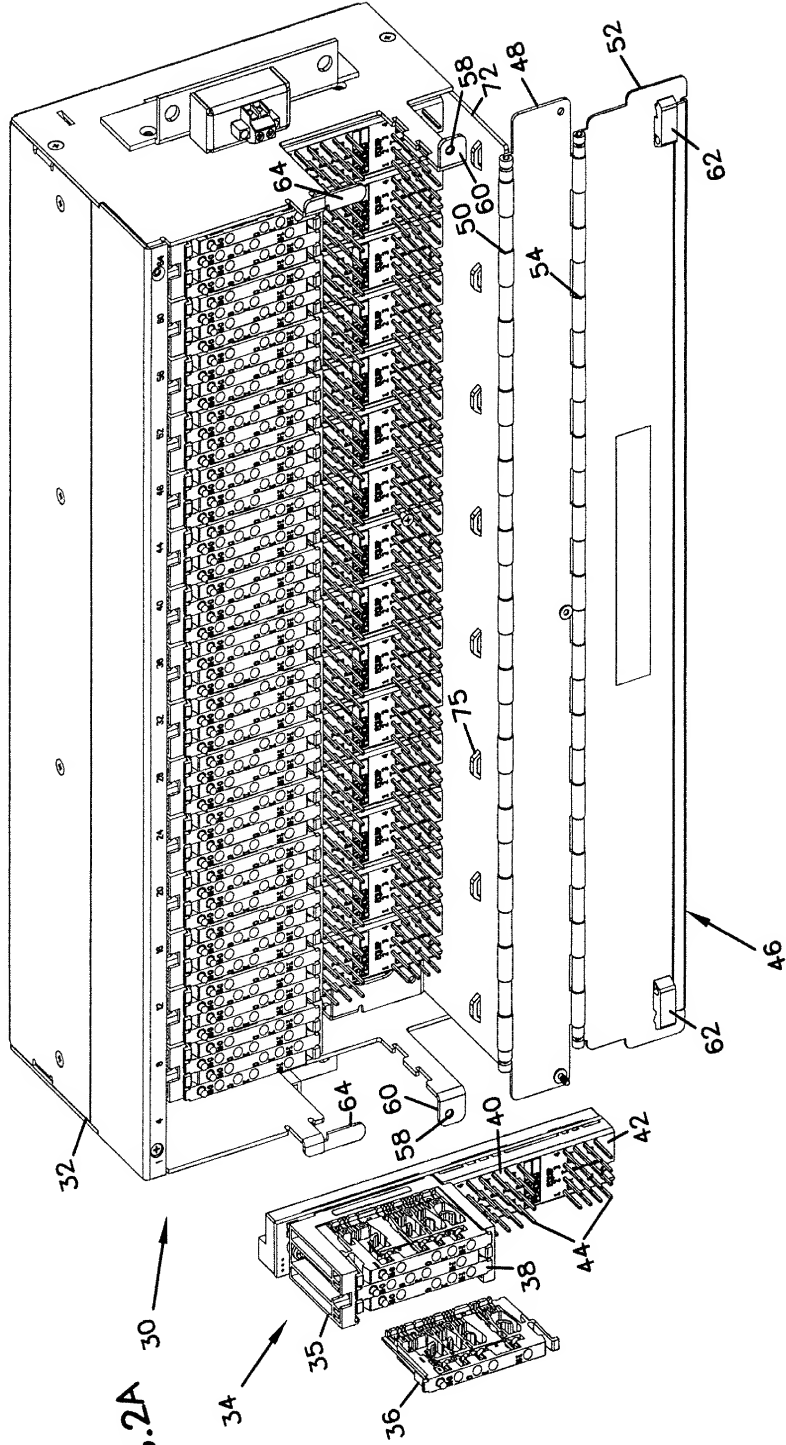




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**FIG. 2A**

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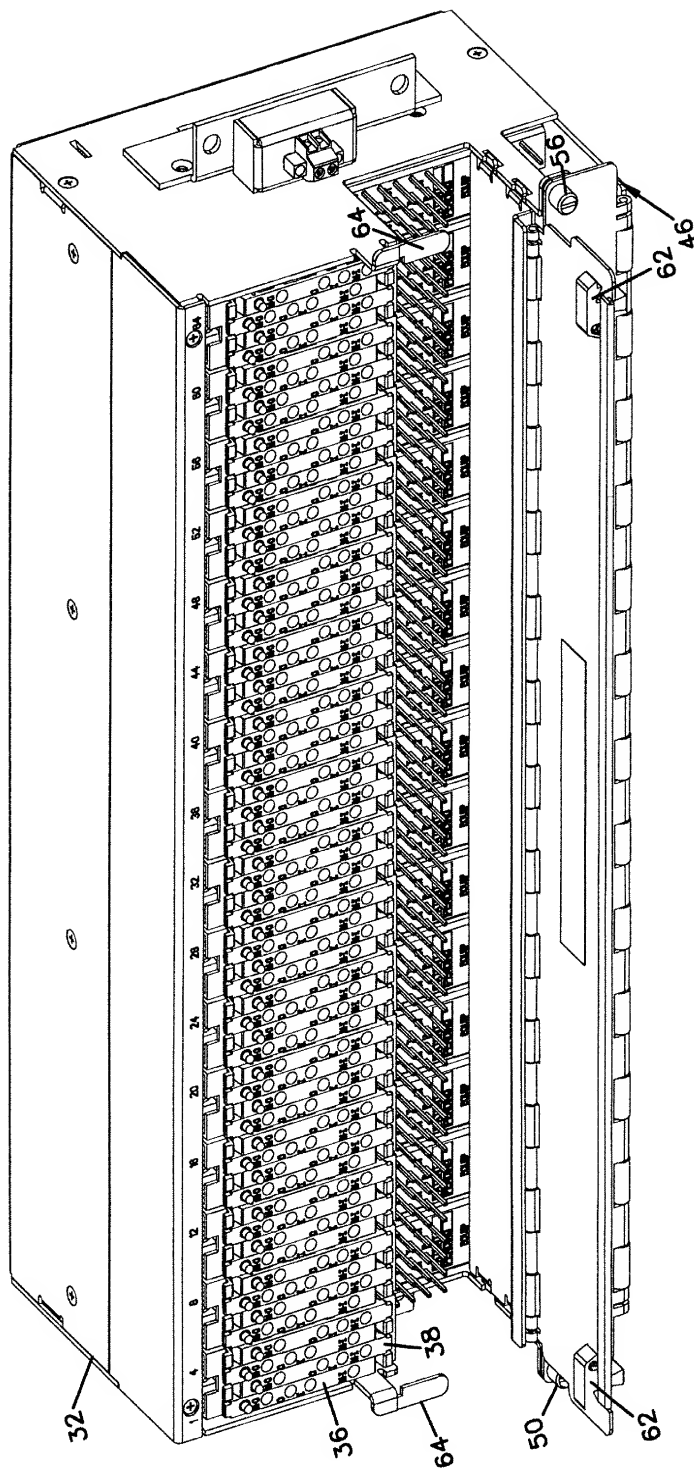


FIG. 2B

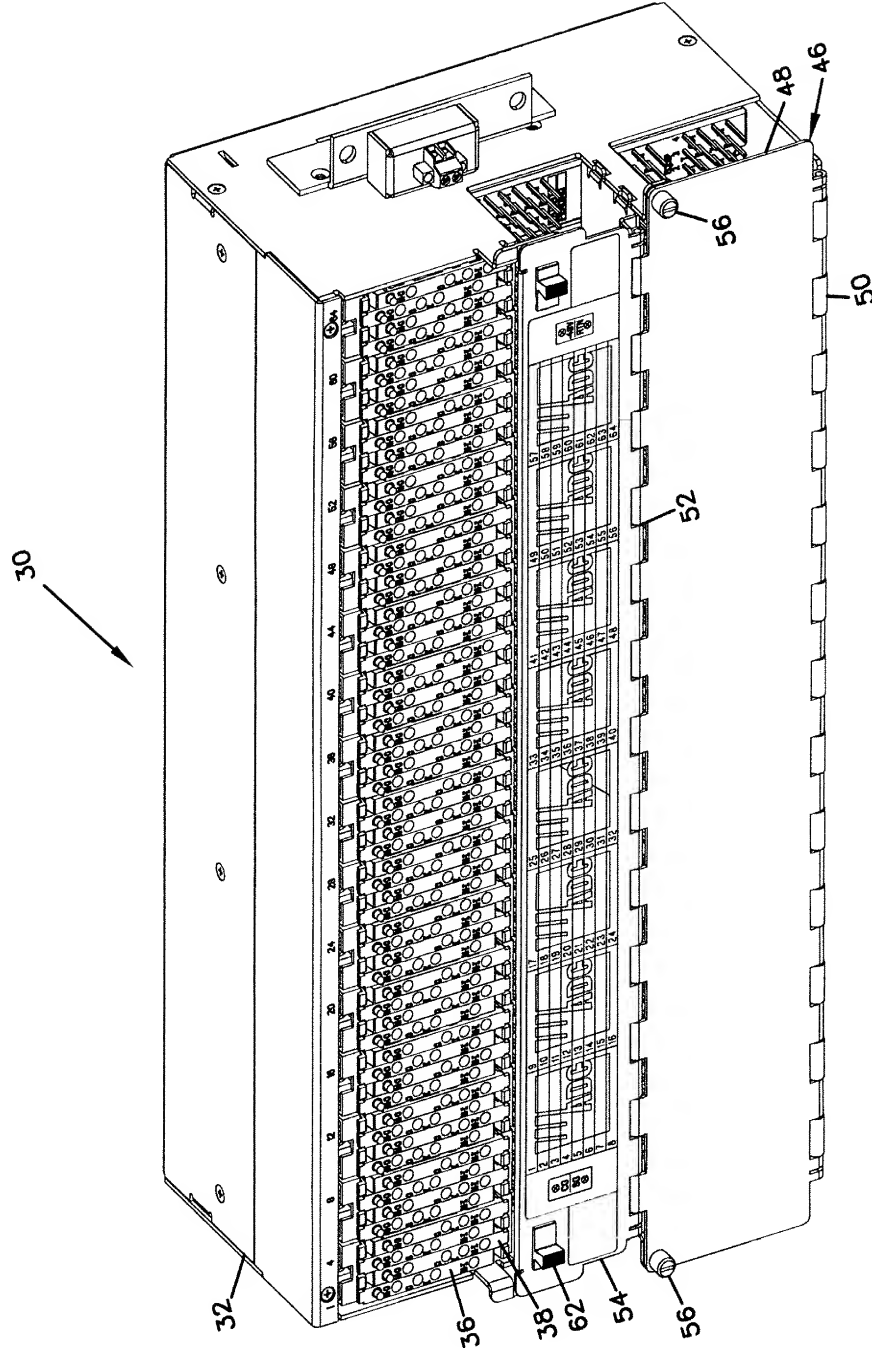


FIG. 2C

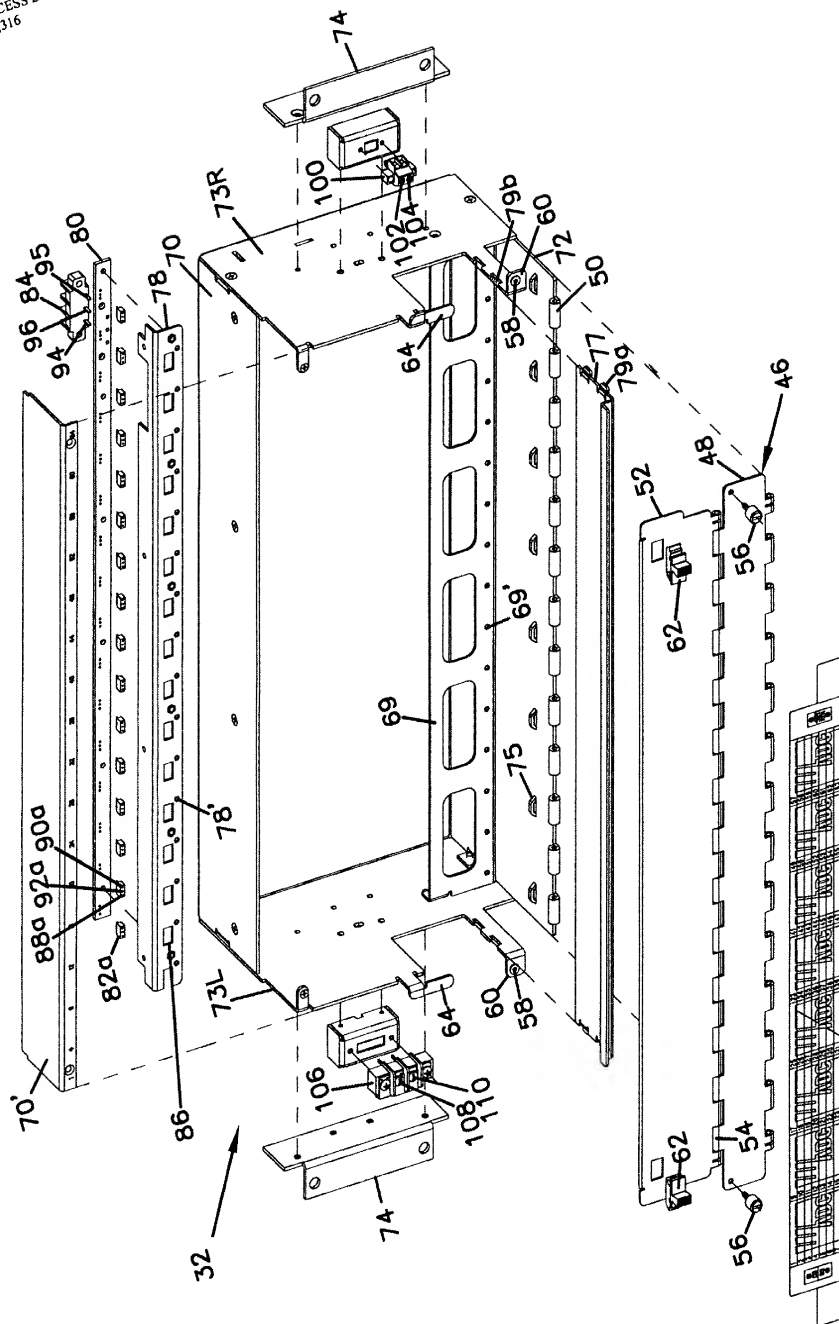


FIG. 3

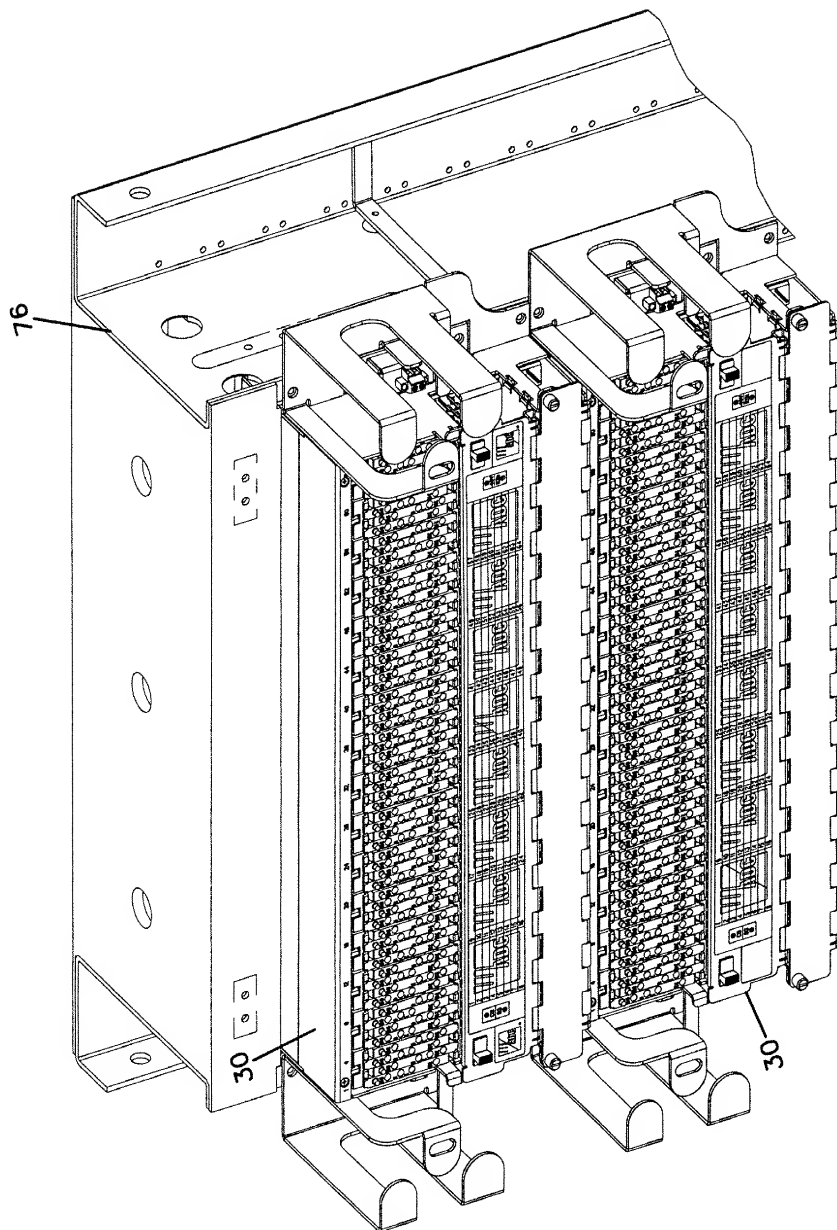
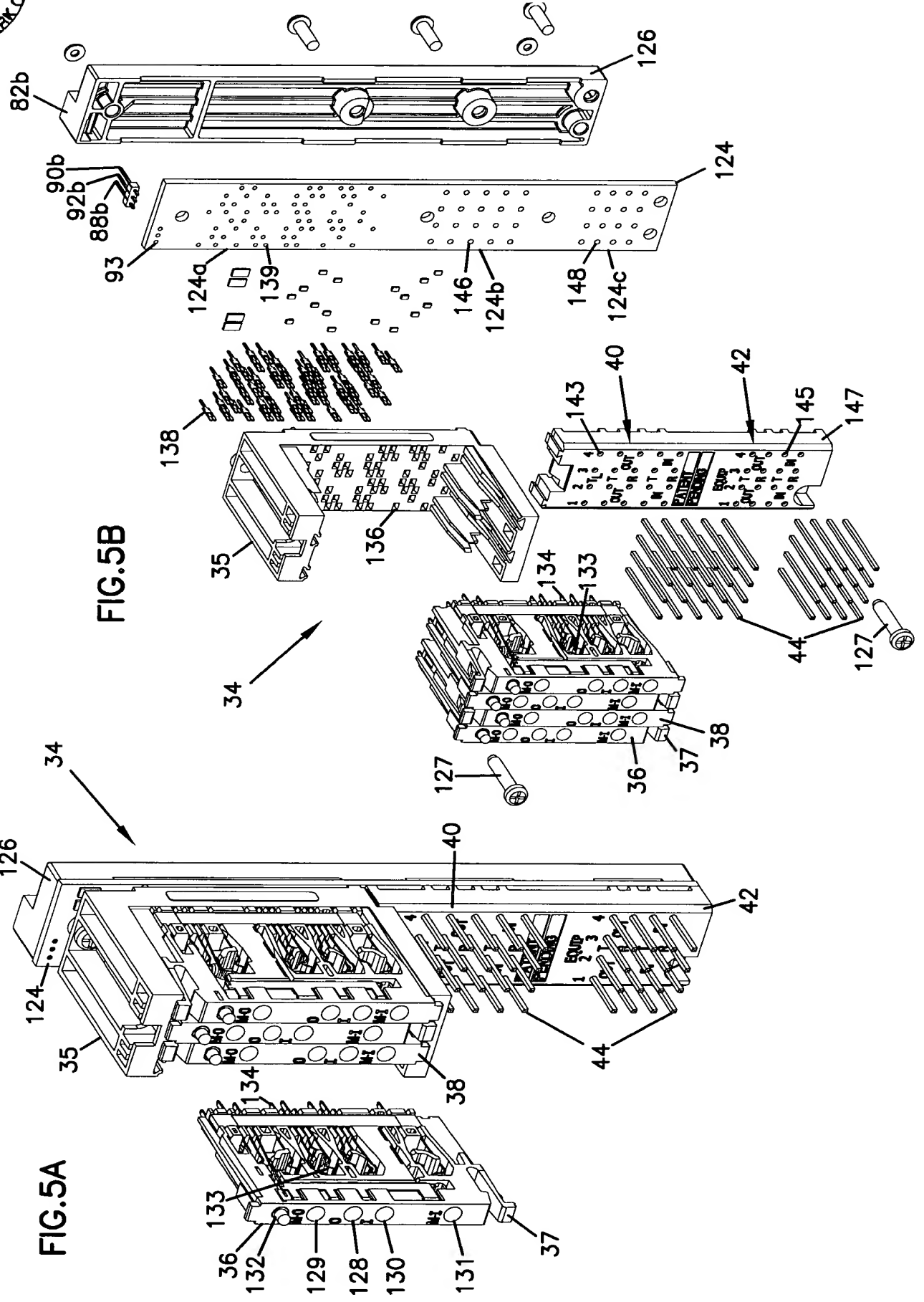
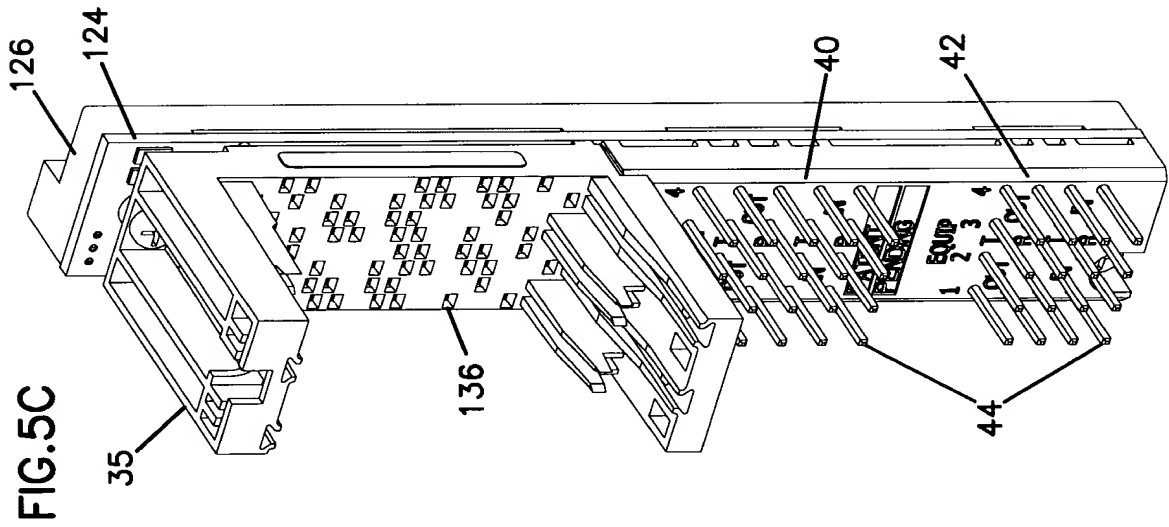
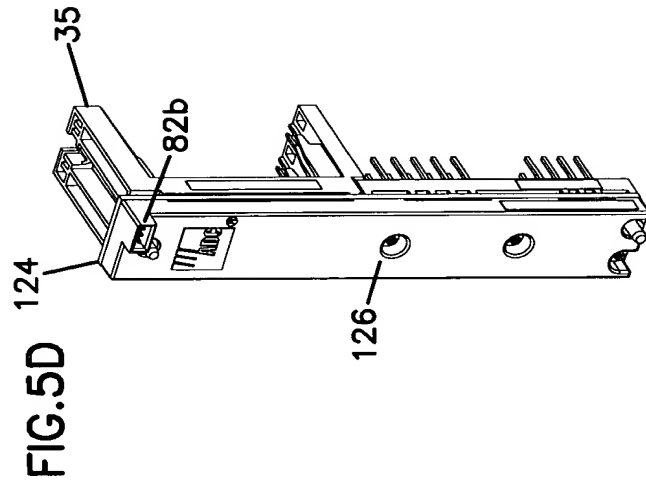


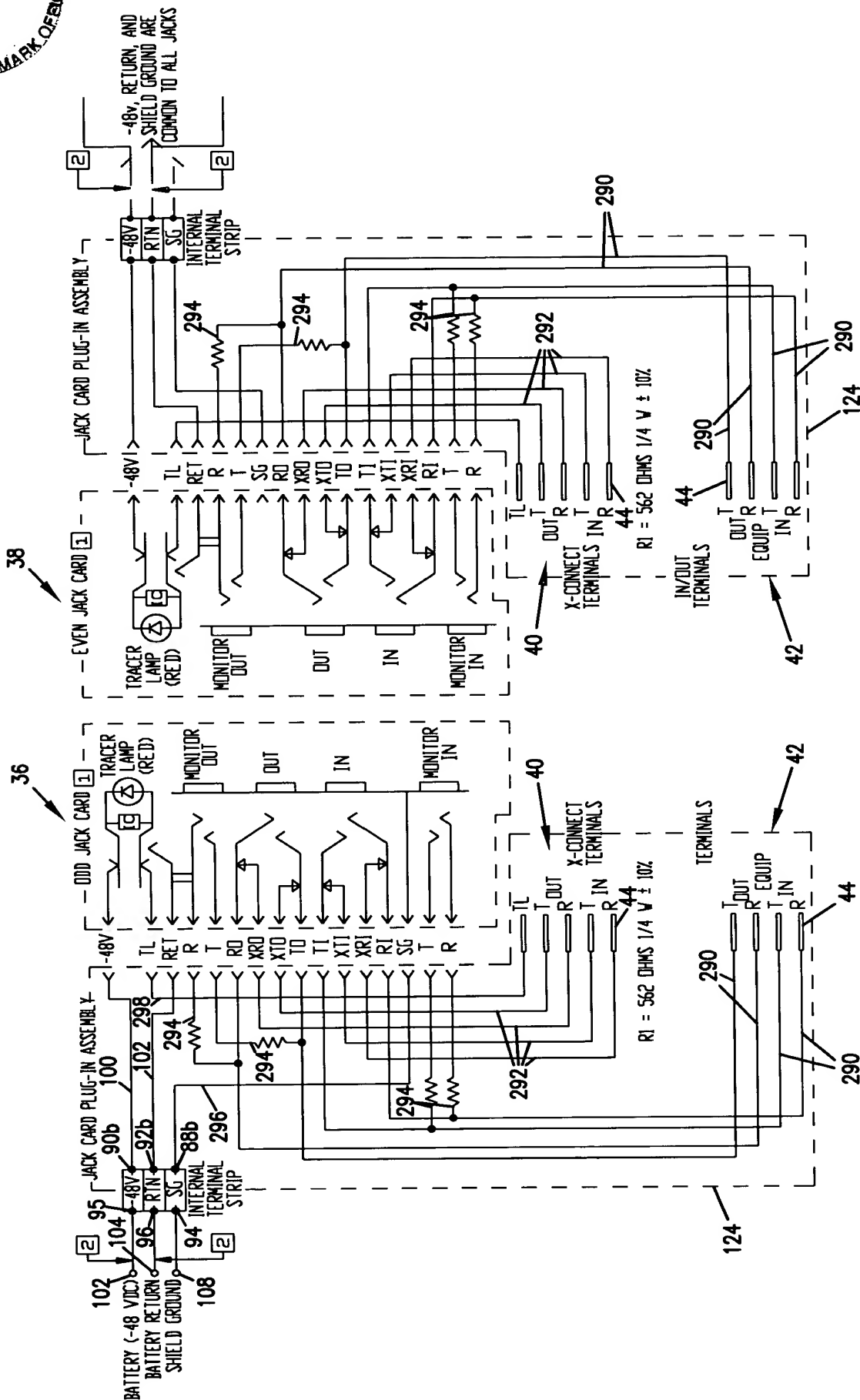
FIG. 4

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**FIG. 6B** CIRCUIT SCHEMATIC  
EVEN JACK POSITION

**FIG. 6A** CIRCUIT SCHEMATIC  
ODD JACK POSITION

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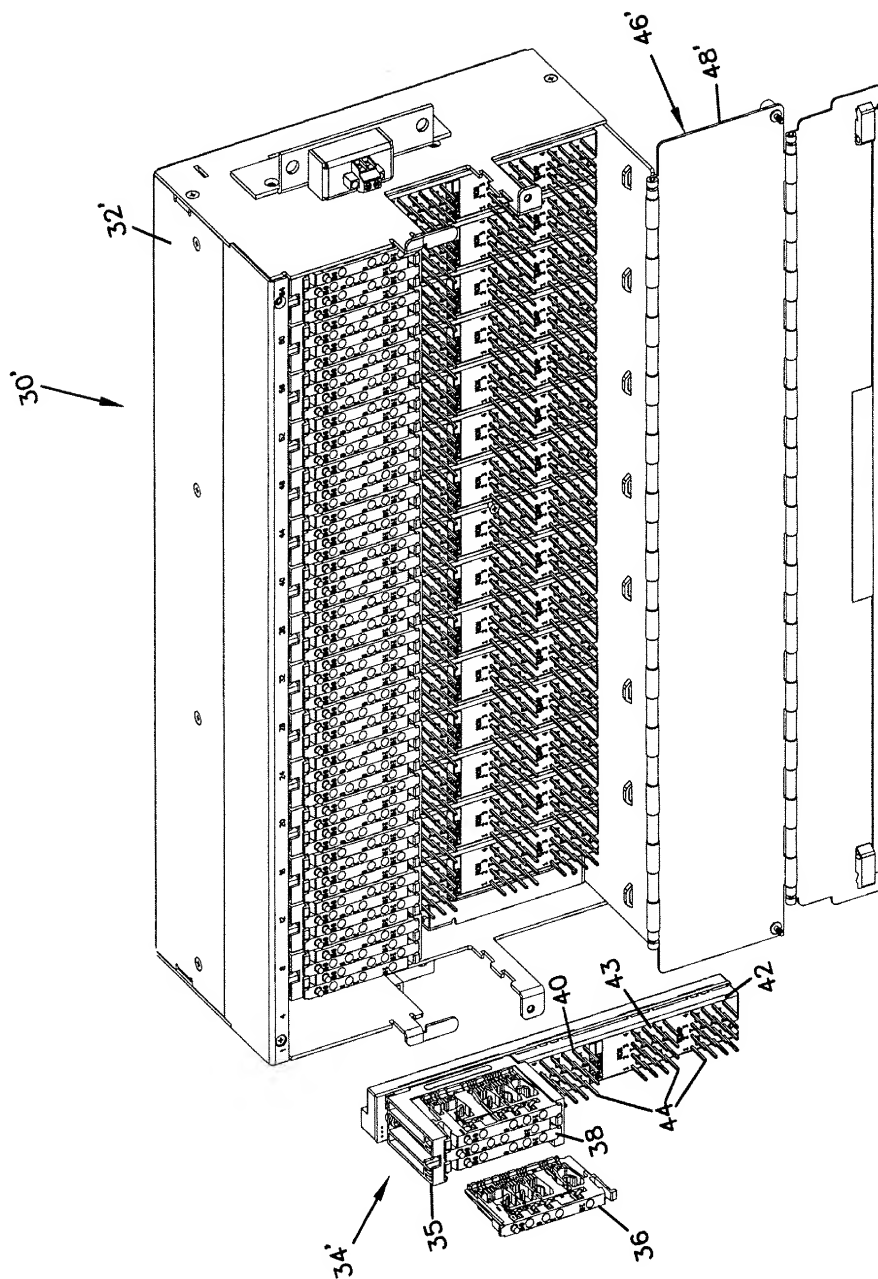


FIG. 7A



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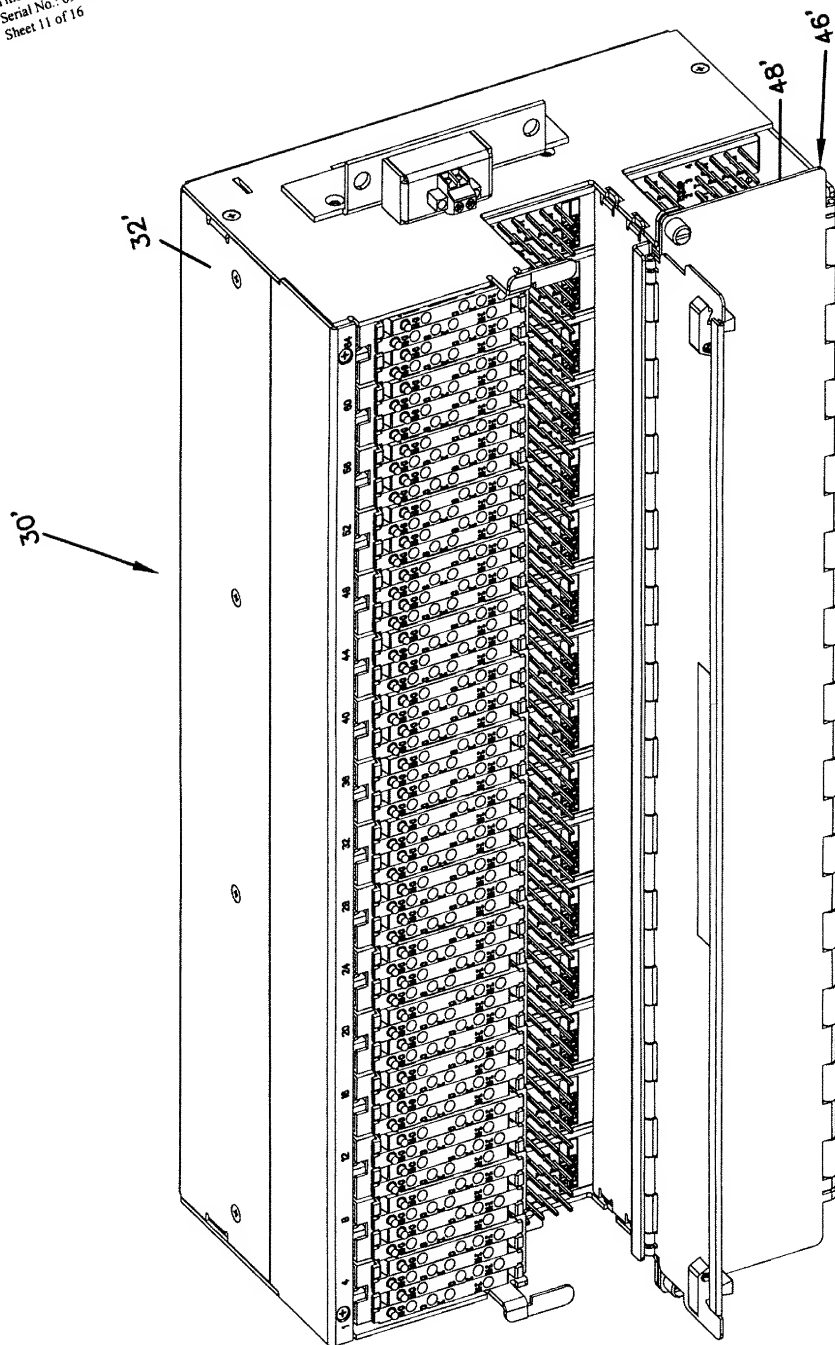


FIG. 7B



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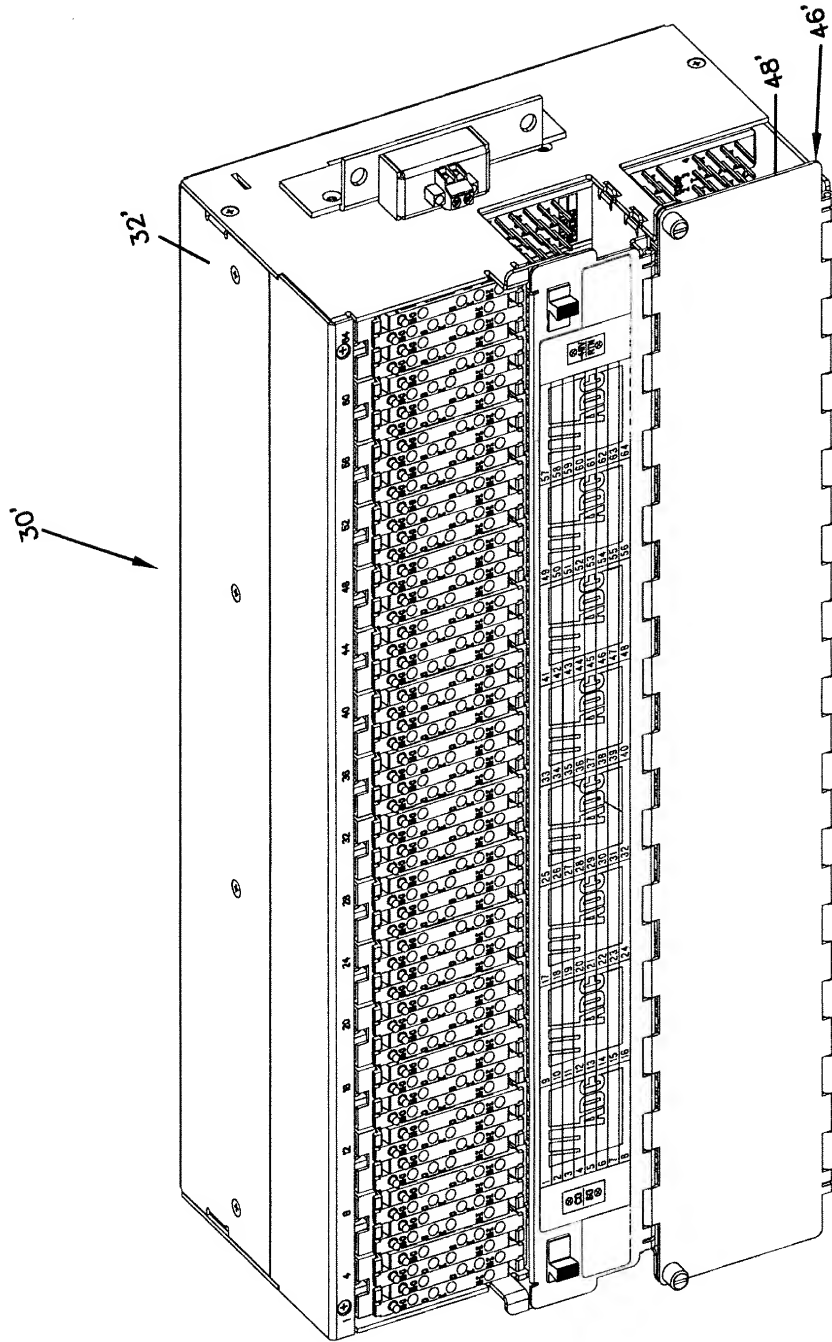


FIG. 7C



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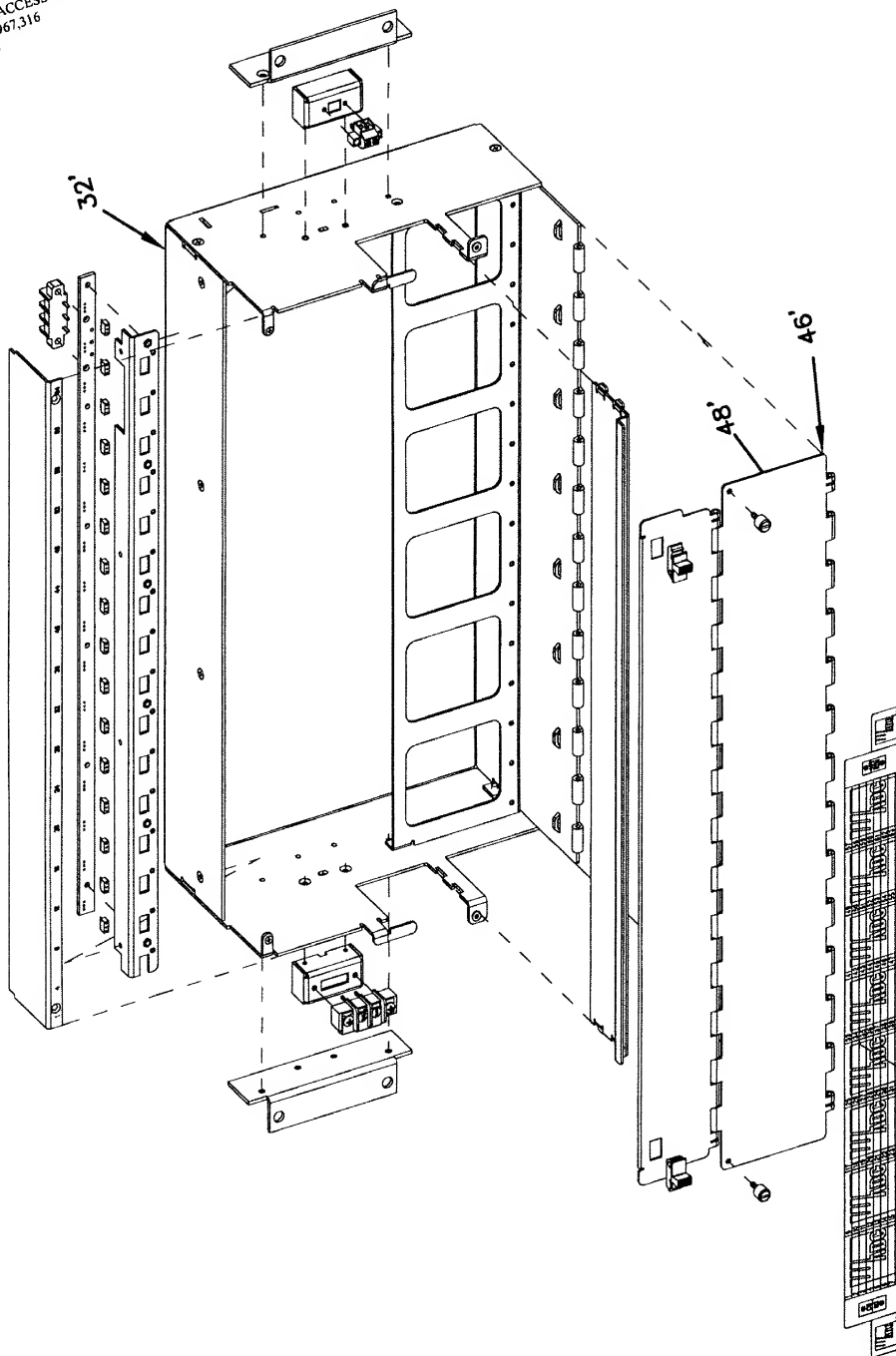
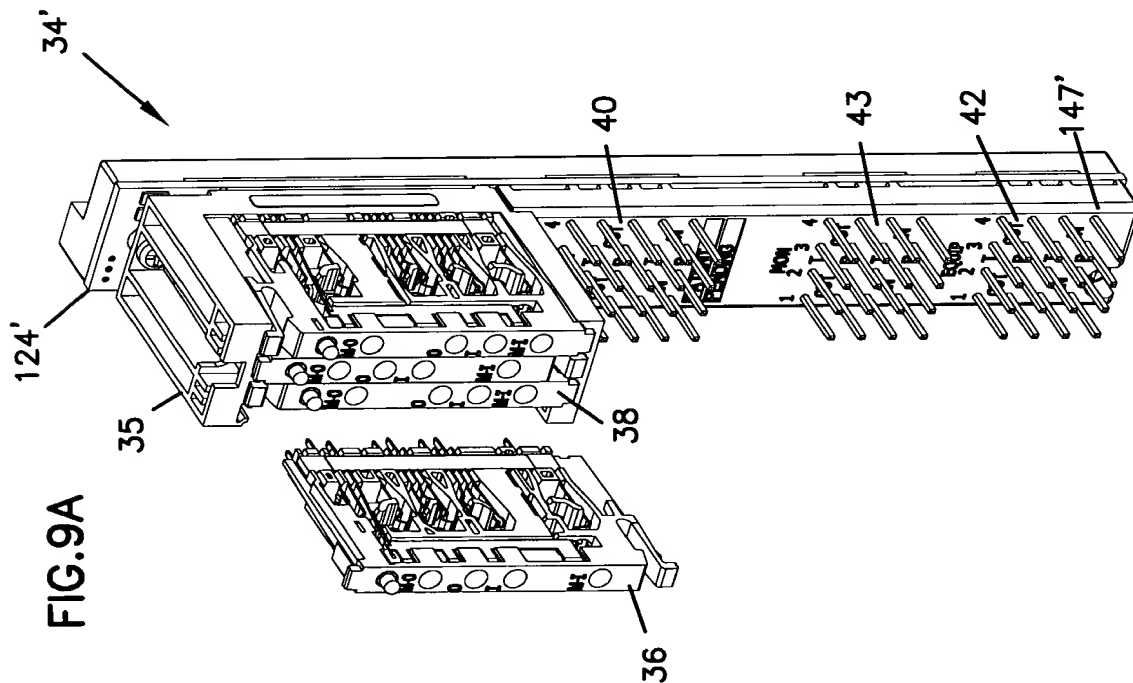
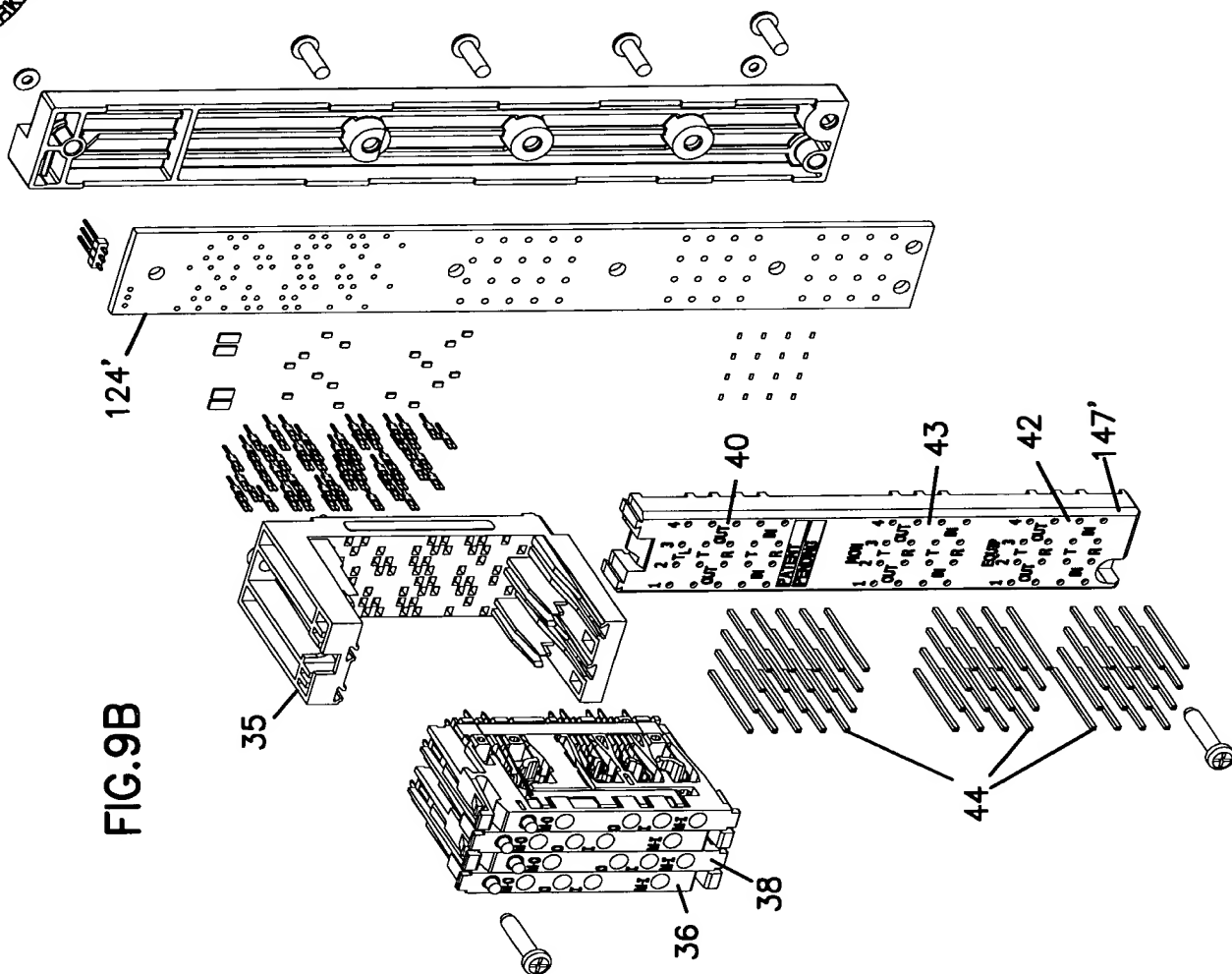


FIG. 8





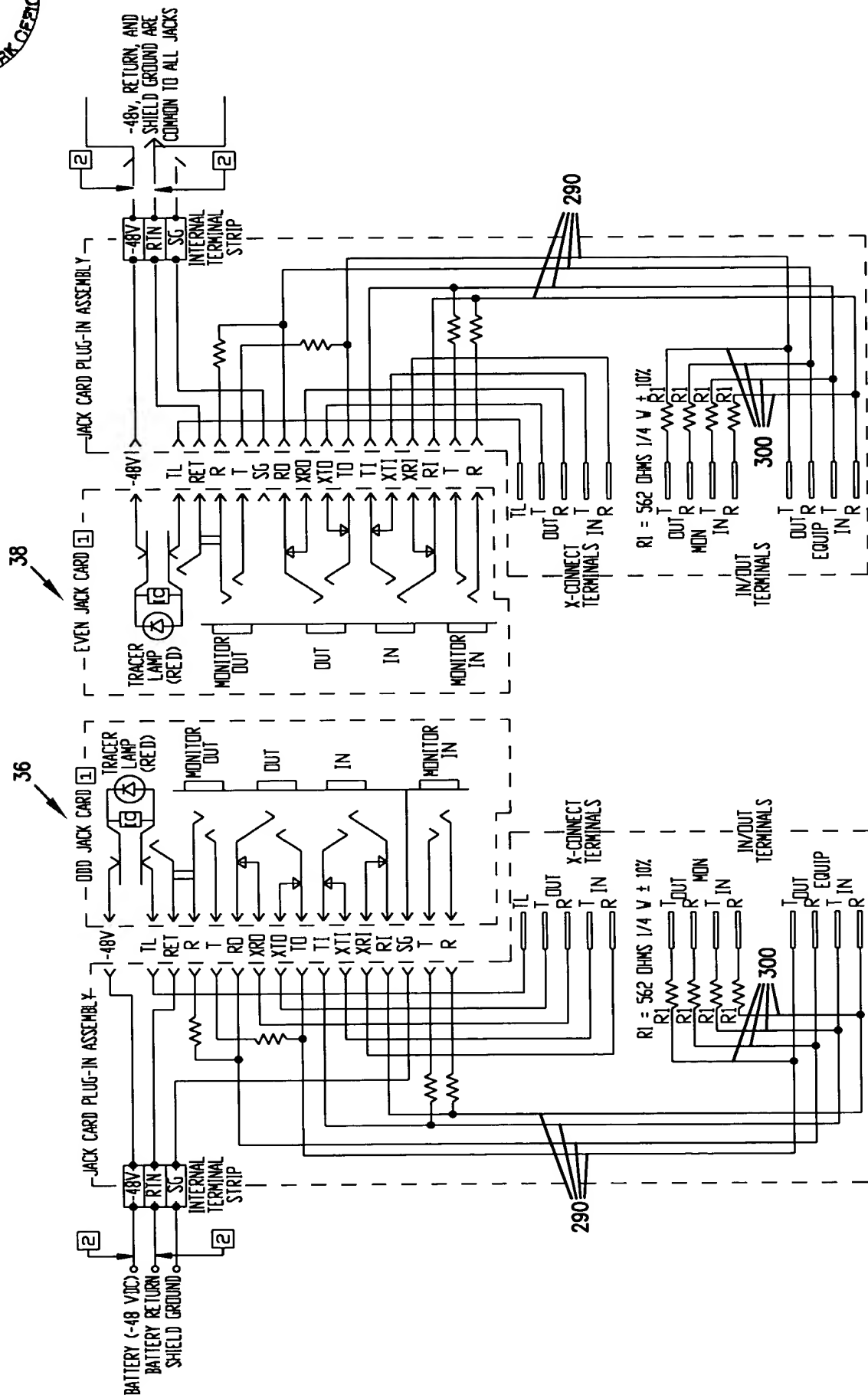


FIG.10B  
 EVEN JACK POSITION

FIG.10A  
 ODD JACK POSITION